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Chips: Broadcom Announces Single-Chip Cable Modem Solution for Europe; New ITU-Compliant Chip Enables Interoperable Cable Modem Standard for Europe.(customized BCM3300 for European market)(Product Announcement)

[EDGE, On & About AT&T](#), Oct 26, 1998

Broadcom Corporation, a leading developer of integrated circuits enabling high-speed broadband communications to the home and business, Monday announced the availability of the industry's first single-chip communications solution for cable modems customized for the European market. The chip is compliant with the recently-approved International Telecommunications Union (ITU-T) J.112 cable modem standard and will provide European cable operators and consumers with interoperable cable modems at extremely competitive prices. Broadcom developed the chip to address the accelerating demand for high speed Internet access and Voice over IP (VoIP) applications within Europe.

"Broadcom's single-chip solution represents a significant milestone in the evolution of cable modems worldwide," said Tim Lindentfeller, Broadcom's Vice President of Marketing. "Developing a customized version of our BCM3300 allows us to provide a single-chip solution for the growing European interest in cable modems and high-speed Internet access. This chip will enable manufacturers to deliver advanced cable modems using an established standard that already has production-ready products that will be available at very attractive consumer price points. European cable operators are now in a very competitive position to provide high-speed Internet access over cable, and, by offering features such as telephony over cable, they can generate profitable new revenue streams."

About the BCM3300 The new Broadcom QAMLink cable modem chip, the BCM3300, represents the industry's first single-chip cable modem solution that includes the ITU-T J.112-compliant Media Access Controller (MAC) and DVB-compliant physical layer (PHY) transmission functions. The integrated receiver supports the DVB cable transmission Baseline System, which is based on QAM modulation with 16, 32, 64, 128 and 256-QAM constellation points together with a very powerful Forward Error Correction (FEC) decoder.

The receiver in the BCM3300 incorporates a programmable gain amplifier, a 10-bit analog-to-digital (A/D) converter, a digital demodulator, Nyquist filters, tracking loops, an adaptive-decision feedback equalizer, and a DVB/DAVIC-compatible forward error correction (FEC) decoder. The receiver supports variable baud rates up to 7 Mbaud to support data rates up to 56 Mbps using 256-QAM in an 8 MHz channel. The integrated transmitter features a programmable FEC encoder, a variable rate QPSK/16-QAM modulator capable of 20 Mbps upstream data rates, and a 10-bit D/A converter which

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The integrated MAC supervises the upstream and downstream functions as specified by the ITU-T J.112 MAC protocol, including advanced Quality of Service (QoS) features, fragmentation of packets to support constant bit rate services such as VoIP, sophisticated filtering to support multicast services and multiple service IDs so the user can have more than one PC connected to the modem. The integrated MAC also includes baseline privacy encryption/decryption for Internet security with 40/56-bit DES Cipher Block Chaining with Residual Block Termination. The MAC is based on a variable length Internet Protocol (IP) packet architecture.

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